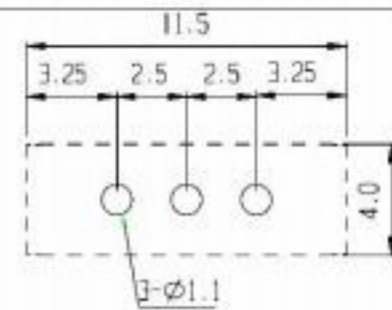


SCHEMATIC



PCB LAYOUT

