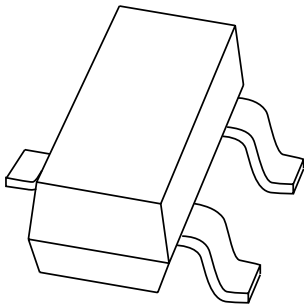


# DATA SHEET



## **MMBTA42** NPN high-voltage transistor

Product specification

2000 Apr 11

NPN high-voltage transistor

MMBTA42

FEATURES

- Low current (max. 100 mA)
- High voltage (max. 300 V).

APPLICATIONS

- Telephony
- Professional communication equipment.

DESCRIPTION

NPN high-voltage transistor in a SOT23 plastic package.  
PNP complement: MMBTA92.

MARKING

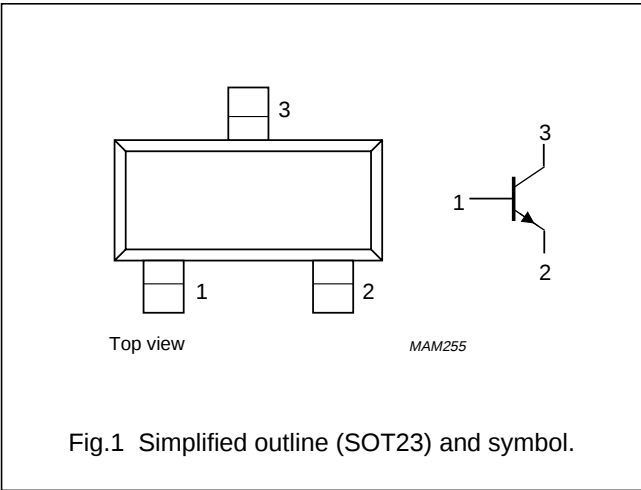
| TYPE NUMBER | MARKING CODE <sup>(1)</sup> |
|-------------|-----------------------------|
| MMBTA42     | 7D*                         |

Note

1. \* = p: made in Hong Kong.  
      \* = t: made in Malaysia.

PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | base        |
| 2   | emitter     |
| 3   | collector   |



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL           | PARAMETER                     | CONDITIONS                       | MIN. | MAX. | UNIT |
|------------------|-------------------------------|----------------------------------|------|------|------|
| V <sub>CBO</sub> | collector-base voltage        | open emitter                     | –    | 300  | V    |
| V <sub>CEO</sub> | collector-emitter voltage     | open base                        | –    | 300  | V    |
| V <sub>EBO</sub> | emitter-base voltage          | open collector                   | –    | 6    | V    |
| I <sub>C</sub>   | collector current (DC)        |                                  | –    | 100  | mA   |
| I <sub>CM</sub>  | peak collector current        |                                  | –    | 200  | mA   |
| I <sub>BM</sub>  | peak base current             |                                  | –    | 100  | mA   |
| P <sub>tot</sub> | total power dissipation       | T <sub>amb</sub> ≤ 25 °C; note 1 | –    | 250  | mW   |
| T <sub>stg</sub> | storage temperature           |                                  | –65  | +150 | °C   |
| T <sub>j</sub>   | junction temperature          |                                  | –    | 150  | °C   |
| T <sub>amb</sub> | operating ambient temperature |                                  | –65  | +150 | °C   |

Note

1. Transistor mounted on an FR4 printed-circuit board.

## NPN high-voltage transistor

## MMBTA42

## THERMAL CHARACTERISTICS

| SYMBOL        | PARAMETER                                   | CONDITIONS | VALUE | UNIT |
|---------------|---------------------------------------------|------------|-------|------|
| $R_{th\ j-a}$ | thermal resistance from junction to ambient | note 1     | 500   | K/W  |

## Note

1. Transistor mounted on an FR4 printed-circuit board.

## CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

| SYMBOL      | PARAMETER                            | CONDITIONS                                                                                    | MIN.           | MAX.        | UNIT |
|-------------|--------------------------------------|-----------------------------------------------------------------------------------------------|----------------|-------------|------|
| $I_{CBO}$   | collector cut-off current            | $I_E = 0$ ; $V_{CB} = 200\text{ V}$                                                           | –              | 100         | nA   |
| $I_{EBO}$   | emitter cut-off current              | $I_C = 0$ ; $V_{EB} = 6\text{ V}$                                                             | –              | 100         | nA   |
| $h_{FE}$    | DC current gain                      | $V_{CE} = 10\text{ V}$<br>$I_C = 1\text{ mA}$<br>$I_C = 10\text{ mA}$<br>$I_C = 30\text{ mA}$ | 25<br>40<br>40 | –<br>–<br>– |      |
| $V_{CEsat}$ | collector-emitter saturation voltage | $I_C = 20\text{ mA}$ ; $I_B = 2\text{ mA}$                                                    | –              | 500         | mV   |
| $V_{BEsat}$ | base-emitter saturation voltage      | $I_C = 20\text{ mA}$ ; $I_B = 2\text{ mA}$                                                    | –              | 900         | mV   |
| $C_{re}$    | feedback capacitance                 | $I_C = I_c = 0$ ; $V_{CB} = 20\text{ V}$ ;<br>$f = 1\text{ MHz}$                              | –              | 3           | pF   |
| $f_T$       | transition frequency                 | $I_C = 10\text{ mA}$ ; $V_{CE} = 20\text{ V}$ ;<br>$f = 100\text{ MHz}$                       | 50             | –           | MHz  |

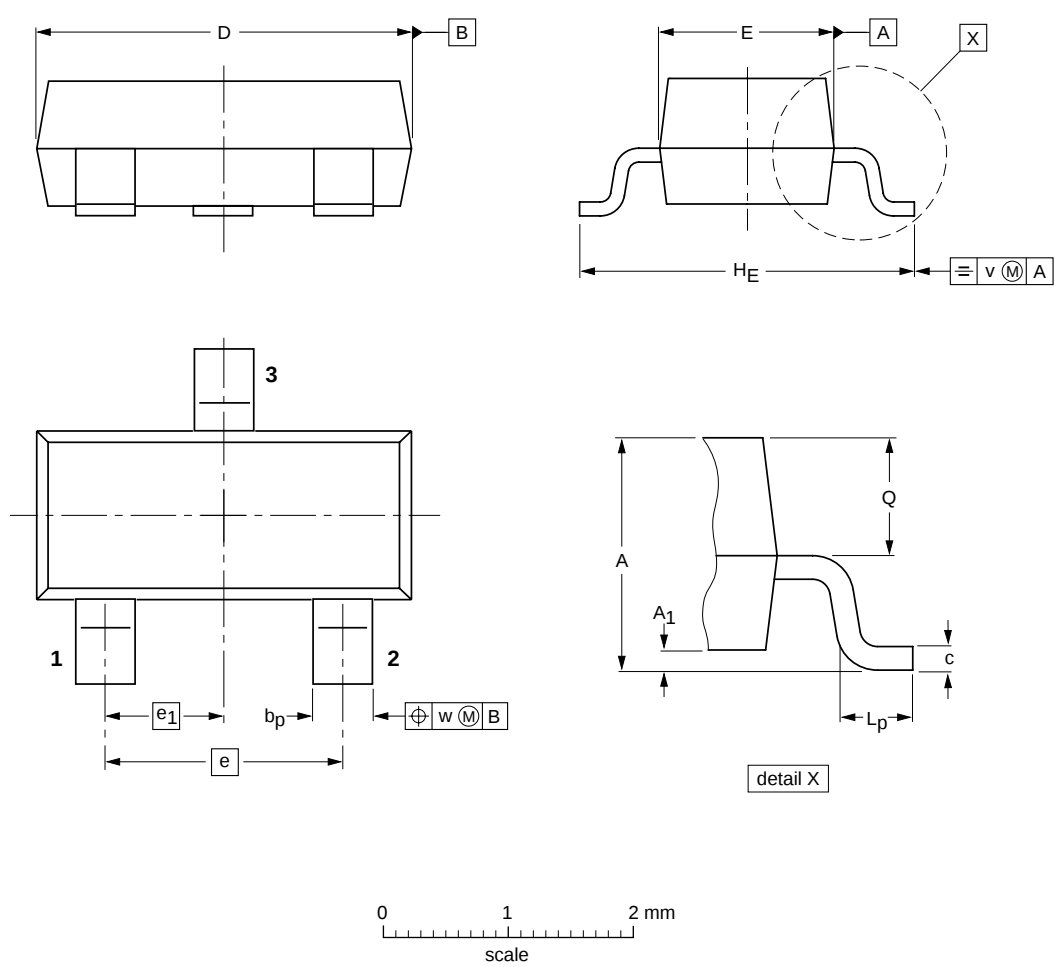
NPN high-voltage transistor

MMBTA42

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

| UNIT | A          | A <sub>1</sub><br>max. | b <sub>p</sub> | c            | D          | E          | e   | e <sub>1</sub> | H <sub>E</sub> | L <sub>p</sub> | Q            | v   | w   |
|------|------------|------------------------|----------------|--------------|------------|------------|-----|----------------|----------------|----------------|--------------|-----|-----|
| mm   | 1.1<br>0.9 | 0.1                    | 0.48<br>0.38   | 0.15<br>0.09 | 3.0<br>2.8 | 1.4<br>1.2 | 1.9 | 0.95           | 2.5<br>2.1     | 0.45<br>0.15   | 0.55<br>0.45 | 0.2 | 0.1 |

| OUTLINE<br>VERSION | REFERENCES |          |      |  | EUROPEAN<br>PROJECTION                                                                | ISSUE DATE                      |
|--------------------|------------|----------|------|--|---------------------------------------------------------------------------------------|---------------------------------|
|                    | IEC        | JEDEC    | EIAJ |  |                                                                                       |                                 |
| SOT23              |            | TO-236AB |      |  |  | <del>97-02-28</del><br>99-09-13 |

## NPN high-voltage transistor

## MMBTA42

## DATA SHEET STATUS

| DATA SHEET STATUS         | PRODUCT STATUS | DEFINITIONS <sup>(1)</sup>                                                                                                                                                                                                                                 |
|---------------------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Objective specification   | Development    | This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.                                                                                                          |
| Preliminary specification | Qualification  | This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product. |
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## Note

1. Please consult the most recently issued data sheet before initiating or completing a design.

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**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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NPN high-voltage transistor

MMBTA42

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**NOTES**

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NPN high-voltage transistor

MMBTA42

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Printed in The Netherlands

603506/01/pp8

Date of release: 2000 Apr 11

Document order number: 9397 750 06939

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